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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/691,584	10/24/2003	Naruhiko Nakanishi	8038-1047	6943

466 7590 10/26/2005

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EXAMINER
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HA, NATHAN W

ART UNIT	PAPER NUMBER
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2814

DATE MAILED: 10/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

10/691,584

Applicant(s)

NAKANISHI, NARUHIKO

Examiner

Nathan W. Ha

Art Unit

2814

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 10 August 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-18 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-18 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## DETAILED ACTION

The previous 112 rejection has been withdrawn in light of the new amendment.

### ***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1-3 and 5-8, 10-12, 14-17 are rejected under 35 U.S.C. 102(e) as being anticipated by Weiner et al. (US 2002/0151107, previously cited, hereinafter, Weimer.)

In regard to claims 1 and 10, in fig. 1, Weimer discloses a method for forming a capacitor, comprising steps of:

depositing a strontium titanate film 32; and

heat treating the strontium titanate film at a temperature between 500-600 degrees C. See sections [0041] and [0042]. In several sections Weiner explicitly discloses a treat meant process that is used prior or after the wet oxidation process using inner gas such Argon (sections [0011], [0033]-0037].) The temperature is further disclosed as 500-700 degrees C.

In regard to claims 2, 7, 11, and 16 Weimer further discloses the heat treating step crystallizes the strontium titanate film which is an amorphous film. See also section [0041], lines 3-15.

In regard to claims 3, 8, 12, and 17, wherein the inert gas includes nitrogen, N<sub>2</sub>. See section [0041] line 8.

In regard to claims 5 and 14, Weimer further discloses:

forming a bottom electrode 30 overlying a semiconductor substrate 2. See also section [0036], line 5;

depositing a strontium titanate film on the bottom electrode;

forming a top electrode 36 on the strontium titanate film. See section [0022] line 6; and

heat treating the strontium titanate film at a temperature as discussed above regarding claim 1.

In regard to claims 6 and 15, includes a silicon layer, polycrystalline. See section [0036], lines 3-5. It should be noted that a silicon nitride is formed thereon. Therefore, the bottom electrode includes plurality of layers.

### ***Claim Rejections - 35 USC § 103***

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 4, 9, 13, and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Weimer as applied to claims 1-3, 5-8, 10-12, and 14-17 above.

In regard to claims 4 and 9, Weimer discloses all of the claimed limitations as mentioned above. Weimer further the heat-treating process can be on order of several minutes. See section [0041] lines 14-17. Weimer, however, does not disclose the exact a time interval for the process as claimed in claims 4 and 9. But the phrase "several minutes" should be approximate to "5 seconds to 5 minutes". In fact, it is obvious since the dielectric layer is formed by using the same process and the same material.

Therefore, it would have been obvious to one of ordinary skill in the art to recognize the similarity and obviousness that these times are approximately same values and not critical because they can be optimized during routine experimentation, or during the process of making the device.

### ***Response to Arguments***

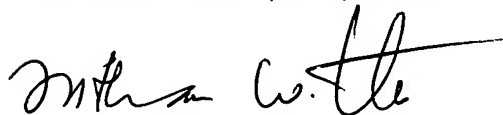
7. Applicant's arguments filed 12/10/04 have been fully considered but they are not persuasive. For instance, Applicants contend that the cited art does not teach that the heat treatment temperature is in the range as claimed. This limitation can be found in section [0033] where the dielectric layer is treated using RTP process in a preferred temperature ranch of about 450-750 degrees C. This range is good when the dielectric is a non-crystalline film, and it approximately covers the range as claimed.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nathan W. Ha whose telephone number is (571) 272-1707. The examiner can normally be reached on M-TH 8:00-7:00(EST).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'Nathan W. Ha', with a long horizontal stroke extending to the right.

Nathan Ha  
October 19, 2005